



1374.43296X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. ICHINOSE, et al.

Serial No.: 10/733,377

Filed: DECEMBER 12, 2003

For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

Group AU: 2822

Examiner: Toniae M. Thomas

Confirm. No.: 8552

**AMENDMENT**

**Mail Stop: AMEND – FEE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

February 28, 2006

Sir:

In response to the Office Action mailed November 30, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.

03/01/2006 JADD01 00000032 10/733,377

01 FC:1201  
02 FC:1202

400.00 OP  
50.00 OP